

EXHIBIT A

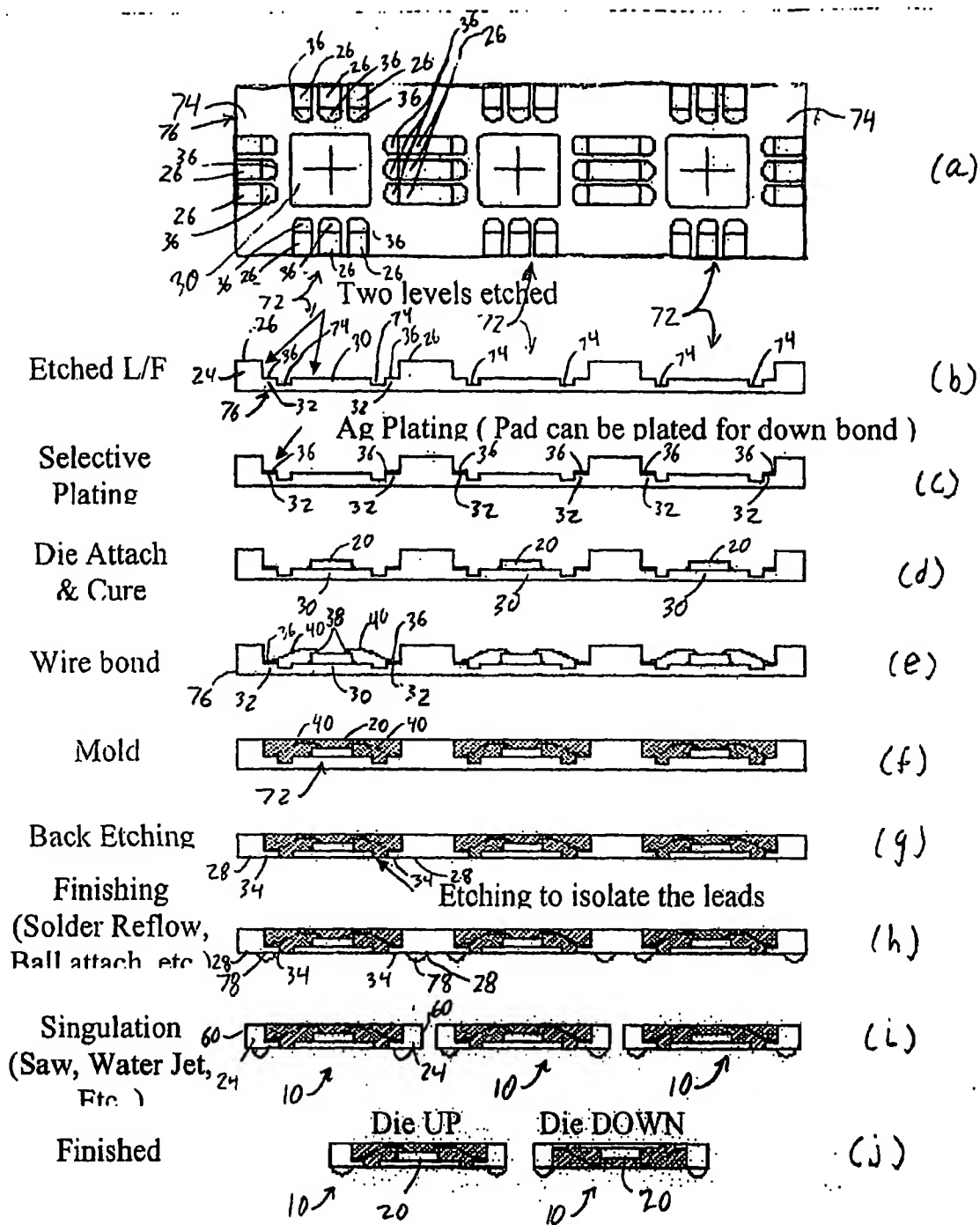


Fig. 6

EXHIBIT B

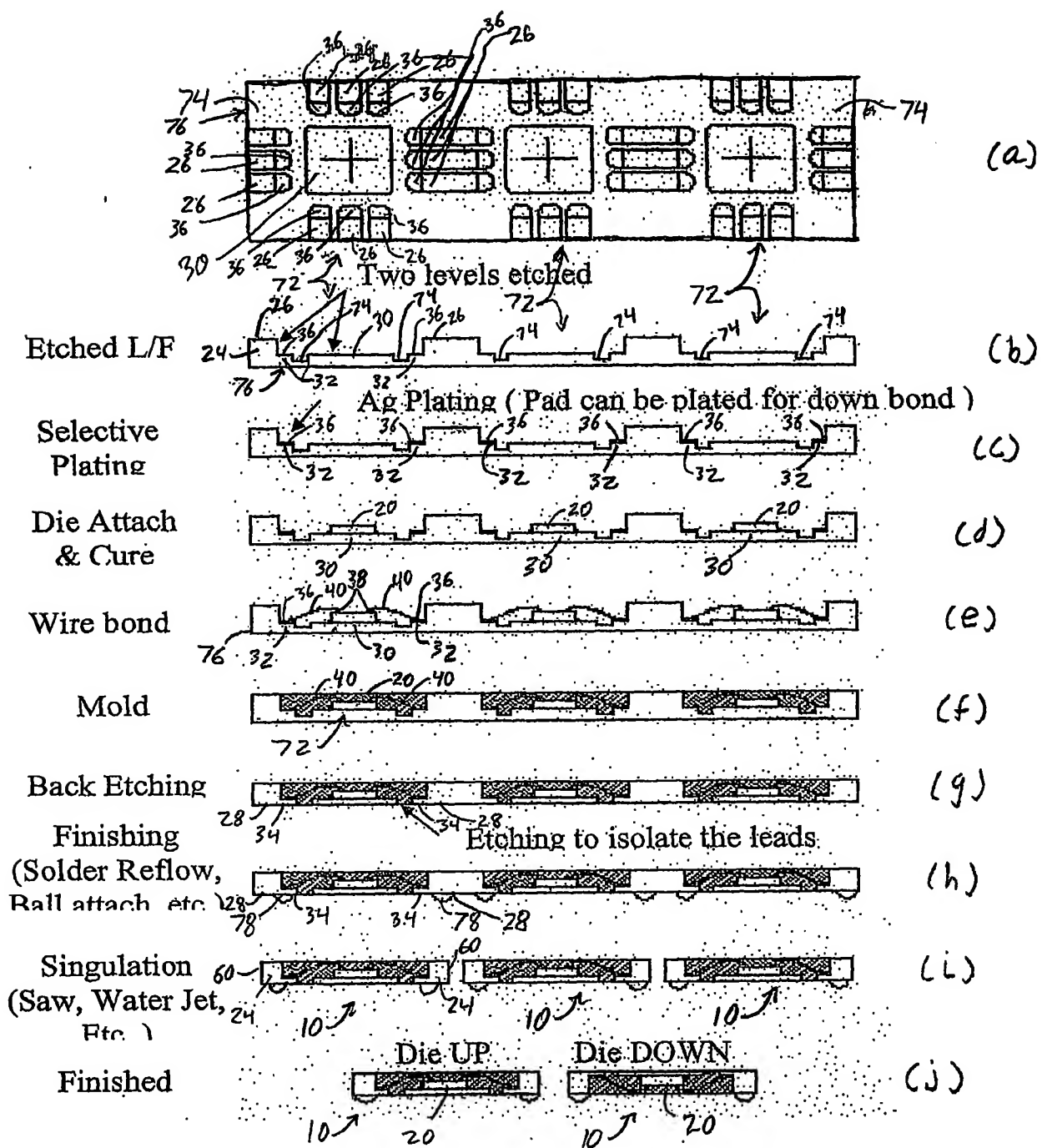


Fig. 6

EXHIBIT C

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Applicant/Advanced Interconnect Technologies
INVENTORS: Limited

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X CORRECTION OF DEFECTS IN THE INTERNATIONAL
APPLICATION UNDER PCT ARTICLES 3(4)(i) and
14(1) and Rule 26 (2 pages)

X FORMAL DRAWINGS - 22 SHEETS

DT03 Rec'd PCT/PTO 14 JAN 2005